

Work Address

Dream Institute of Technology

Thakurpukur, B.H Road
Kolkata : 700104
Room No : C-222



Residential Address

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ABHISHEK SAHA

Objective

Upgrading the educational process and ensuring an interactive environment in which teaching, research, training, upgrading skills and practical applications are integrated so that the student's experience is enriched.

Experience

August, 2008 to till Date	Dream Institute of Technology <i>Asst. Prof. of Electronics & Communication Engineering Department</i>
July, 2011 to August, 2015	University of Calcutta (Rajabajar Campus) <i>Worked as a guest lecturer at Department of Applied Physics</i>
July, 2007 to July, 2008	Maharaja Manindra Chandra College <i>Worked as a part time lecturer at Department of Electronic Science</i>

Present Status

- Presently working as **Assistant Professor** in ECE Department, **Dream Institute of Technology**.
- *Ph.D Research scholar* in **NIT Mizoram** [Enrollment No. **DT20EC001**]

Education

M.Tech in **Microelectronics & VLSI Technology**
West Bengal University of Technology (*in house*) now known as **Maulana Abul Kalam University of Technology**, Kolkata, West Bengal in 2009

M.Sc. in **Electronic Science**
University of Calcutta in 2007

B.Sc in **Electronic**
University of Calcutta in 2004

Software Proficiency

VHDL, VERILOG, PSPICE, TSPICE, MATLAB, Atlas of Silvaco, Medici of Synopsys

Professional Membership

- International Association of Engineers (IAENG)
Membership Number: 235073
- IEEE Student Member (Membership Number:
235073)

Workshop Attended

- Participated in the Workshop on ‘Introduction to VLSI Design Flow with Synopsys’ Tools’ organized by *West Bengal University of Technology*, 2007
- IEEE Workshop on ‘Recent Trends in Image and Video processing’ organized by *West Bengal University of Technology*, 2008
- Participated in the Workshop on ‘Android Tools’ organized by *Dream Institute of Technology*, 2012
- Attended one-day seminar on ‘MATLAB for Data Analytics and Computer Vision’ organized by MATHWORKS, September, 2015
- Participated in the NPTEL SPOC workshop at *IIT-K*, January, 2018
- Participated in the SWAYAM-NPTEL SPOC workshop at *IIT-K*, July, 2018
- Participated in the SWAYAM-NPTEL SPOC workshop at *IIT-K*, February, 2019

Workshop Organized

- Organized Android Workshop in Association with IIT-Guwahati, November, 2013
- Organized ‘Space Technology Awareness Camp in Association with Space Technology Students’ Society, 2017
- Organized **IEEE-EDS** sponsored Faculty Outreach Program, August, 2019
- Organized one-day webinar on ‘*Advances in Tracking RADAR Systems for Test Range Application*’ in collaboration with **IEEE-MTT-s SBC** and **Jadavpur University**, July, 2020

Job Profile

- Assistant Professor in ECE Department, *Dream Institute of Technology*.
- Departmental **In Charge**, *Dream Institute of Technology*.
- **SPOC SWAYAM-NPTEL Local Chapter**, *Dream Institute of Technology*.
- **Routine Committee** Convener, *Dream Institute of Technology*.
- **Institute’s Nodal Officer**, *Swachh Bharat Abhiyan Summer Internship Program*.
- Project Co-Ordinator Unnat Bharat Abhiyan (Coordinating institute IIT-Delhi).
- **IQAC Co Ordinator**

Publication

- 1) Abhishek Saha, Rudra Sankar Dhar, Subhro Ghoshal, “A Comparative study of GaN and Si based SOI FinFET” in International Conference on Computational Techniques and Applications (ICCTA, 2021)
- 2) Abhishek Saha, Vedatrayee Chakraborty, “Simple Gain Model of Group III-V Semiconductor based Laser Diode” Recent Trends in Electronics & Communication Systems, ISSN: 2393-8757 Volume 7, Issue 3, pp 9-27.
- 3) Abhishek Saha, Vedatrayee Chakraborty, “Comparative study of Drain Current of Symmetric and Asymmetric DGMOSFET with Simulation in Silvaco_TCAD Software” in Journal of Semiconductor Devices and Circuits, ISSN: 2455-3379, Volume 8, Issue 1, pp 1-14, 2021
- 4) Sourav Mukherjee, Abhishek Saha, Madhuchanda Mitra, ‘ECG Feature Extraction: Lagrange Five Point Interpolation and Hilbert Transform Based Approach’ at NaCCS, 2012. (Bardhaman University)

Subject taken in UG Level

- Basic Electronics
- Digital Electronics
- Analog Electronic Circuit
- Solid State Device
- Microelectronics & VLSI Design
- EDA for VLSI Design

Subject taken in PG level

- VLSI Device & Modelling
- Digital IC design
- Analog IC design
- CAD tools for VLSI Design
- Low power VLSI design

