

Work address
Dream Institute of Technology, Kolkata

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Sabir Akhtar Mallick

- Objective** Research and development position for solving challenging problems in VLSI Architecture, VLSI Testing, DSP Algorithms & Architecture Design, Approximate Architecture Design, Internet of Things.
- Experience** Jan.2018 – current Dream Institute of Technology, MAKAUT, **Electronics and Communication Engineering**, Kolkata, India
Assistant Professor of ECE
- Aug. 2017 – Dec. 2017 University Institute of Technology, Burdwan University
Electronics and Communication Engineering, Burdwan, India
Assistant Professor of ECE
- Education** **M.Tech., Electronics and Communication Engineering, Aliah University**, Kolkata, India, June 2016 CGPA: 7.9/10.0
Thesis: In-Circuit Configurable Montgomery Multiplication Architecture for Special Classes of Polynomials over $GF(2^m)$.
(Advisor: Asst. Prof. S. Talapatra)
VLSI Architecture.
- B.Tech., Electronics and Communication Electronics Engineering, Aliah University**, Kolkata, India, CGPA: 7.42/10.0
Thesis: Unified and Low Complexity Digit Serial Systolic Montgomery Multiplication Architecture for Special Classes of Polynomials over $GF(2^m)$.
(Advisor: Asst. Prof. S. Talapatra)
VLSI Architecture.
- Computers** VHDL, XILINX ISE, TSPICE, MATLAB, C programming.
- Technical Skills** Knowledge of VHDL circuit design techniques for VLSI Architecture Design, Approximate Architecture Design, DSP Algorithms & Architecture Design, Image Processing.
Knowledge of CMOS VLSI Design Techniques.
Experience with electronic simulation tools
(XILINX ISE, TSPICE, ARDUINO, AVR STUDIO)
- Post Graduate Course Topics** Digital VLSI Architecture, VLSI Digital Signal Processing Systems, Antennas, Microwaves, RF circuit design, Advance Microwave Theory, Advanced Communication Theory, Advance Digital Communications, Wireless Communications, Advance Optical Communication, Advance Digital Signal Processing, Probability and Statistics

Graduate course topics

Digital VLSI Architecture, CMOS Design, Digital Electronics, Digital Signal Processing, Image Processing, Electromagnetic, Microwaves, RF circuit design, Antennas, Microwave Electronics, Wireless communications, Optical Communication, Satellite Communication, Signal & System, Network Analysis, Control System, Computer Architecture and Organization, Network Communication, Computer Programming Language.

Achievement

Two Times WBSET Qualified (23rd and 24th)

Publication

SABIR AKHTAR MALLICK, SOMSUBHRA TALAPATRA, “LOW LATENCY DIGIT-SERIAL IN-CIRCUIT CONFIGURABLE MONTGOMERY MULTIPLIER INGF(2M) FOR SPECIAL CLASSES OF POLYNOMIAL”, ISSN: 2349-5162, Vol.6, ISSUE 6, JETIR JOURNAL.

References

Available upon request.