

Curriculum Vitae

Dr.Dipankar Sarkar
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Dream Institute of Technology
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PERSONAL INFORMATION:

Date of Birth : 25th July 1973.
Address : S/o S.P.Sarkar,
109/E, Block-F, New Alipore, Kolkata.
West Bengal - 700053.
Sex : Male
Marital Status : Married.
Religion : Hindu
Nationality : Indian
Language Known : English, Hindi, Bengali and Oriya.

ACADEMIC DETAILS:

Examination Passed	Board/ University	Institution	Year
Ph.D	IIT Delhi	IIT Delhi	2009
M. Tech. Degree in Power Systems	Ranchi University	RIT Jamshedpur	1998
B. Tech. Degree in Electrical Engineering	Utkal University.	Orissa Engg. College.	1996
ISc	ISc, Delhi	St.Thomas'School Kolkata.	1992
ICSE	ICSE, Delhi	St.Thomas'School Kolkata.	1990

B.E PROJECT: Title “Micro-Controller Based Single Phase Protector for Induction Motor”.

M-TECH PROJECT: Title “Development of a MOSFET based Inverter”.

Ph.D PROJECT: Title “Steady State Analysis of a Generalized Impedance Controller (GIC) Based Brushless Generator”.

SOFTWARE KNOWLEDGE: C/C++, Matlab/Simulink, MiPower, PSpice, 3D Cutviewer, AutoCad 3D, LabView, Scada, PSim, Sci-lab, Java, Python, Android Studio, Eclipse, Dialogflow, Arkit, Arcore, Mapbox, Vxworks, Busybox, OpenEmbedded, J2ME, FreeRTOS, Windows IOT .

EXPERIENCE:

1. **Industry:** 2 Years
2. **Research And Development:** 1 Year
3. **Academic:** 12 Years, 6 years as Director and 6 Years as Principal in Dream Institute of Technology, Kolkata.

PUBLICATIONS:

1. "Performance, evaluation of Synchronous Impedance controller based self excited induction generator system." By J.K.Chaterjee, Atul Bansal, Dipankar Sarkar. 2006 IEEE power India conference.10-12 April-2006.ISBN no: 0-7803-9525-5.
2. "Evaluation of Synchronized operation of isolated induction generator with DSP based generalized impedance controller." By J.K.Chaterjee, P.J.Chauhan, Dpankar Sarkar, Haresh Bhare, B.V.Parumal .2010 joint international conference on power electronics, drives and energy systems and 2010 power India .ISBN:978-1-4244-7781-4.
3. "Synchronized operation of DSP based generalized impedance controller with variable speed isolated SEIG for novel voltage and frequency controller."By P.J.Chauhan, J.K.Chaterjee,Dpankar Sarkar,Haresh Bhare,B.V.Parumal. IEEE Transaction on Industry applications (Vol.51, Issue-2,march-april2015).ISSN:139-9367.
4. "An Innovative approach for securiting RFID based card information from digital pick pocketing."Dipankar Sarkar,Manish Roy, Santosh Das. International Journal of Innovations in Electrical Research and Technology (IGIERT), (Vol-5,Issue-10,Oct-2018).ISSN:2394-3696

PROJECTS GUIDED: B.Tech-272, M.Tech- 24, Ph.D-1

ENTREPRENEURSHIP ACTIVITIES: 19 Years experience and have mentored- 10 campus companies, 2 alumni start-ups and 1 community start up.

CONSULTANCY ACTIVITIES: Done consultancy activities with companies like, Manikaran Power, Apex Elevator Company, VI Microsystems, Data Control Systems, Dexler Energy and Electrical Engineering Service Supply Syndicate.

GRANTS RECEIVED: Was the first person of the Eastern region responsible for getting the grant of “Innovation and Entrepreneurship Development Cell (IEDC)”, from The Directorate of Science and Technology, Govt. of India.

DOMAIN OF EXPERTISE: Academics, R&D, Innovation, Patenting, Entrepreneurship, Project development, HR, Placements

DECLARATION:

I hereby certify that the information above stated is true to the best of my knowledge.

Place: Kolkata

Date:

(DIPANKAR SARKAR)